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**Compiler Construction** 

- read Appel chapter 1
- make sure you have a working account
- start brushing up on Java
- review Java development tools
- find the official course webpage http://proglang.informatik.uni-freiburg.de/teaching/compilerbau/2010ws/

What is a compiler?

- a program that reads an *executable* program in one language and translates it into an *executable* program in another language
- we expect the program produced by the compiler to be better, in some way, than the original

What is an interpreter?

- a program that reads an *executable* program and produces the results of running that program
- usually, this involves executing the source program in some fashion

This course deals mainly with *compilers* 

Many of the same issues arise in *interpreters* 

Why study compiler construction?

Why build compilers?

Why attend class?

#### Interest

Compiler construction is a microcosm of computer science

#### artificial intelligence

greedy algorithms, learning algorithms

#### algorithms

graph algorithms, union-find, dynamic programming

#### theory

DFAs for scanning, parser generators, lattice theory

#### systems

allocation and naming, locality, synchronization

#### architecture

pipeline management, hierarchy management, instruction set use

Inside a compiler, all these things come together

Machines are constantly changing

Changes in architecture  $\Rightarrow$  changes in compilers

- new features pose new problems
- changing costs lead to different concerns
- old solutions need re-engineering

Changes in compilers should prompt changes in architecture

• New languages and features

# **Intrinsic Merit**

Compiler construction is challenging and fun

- interesting problems
- primary responsibility for performance

(blame)

- new architectures  $\Rightarrow$  new challenges
- real results
- extremely complex interactions

Compilers have an impact on how computers are used

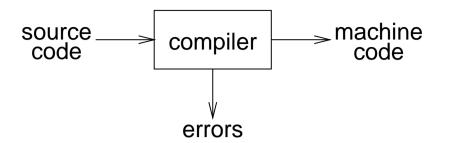
Some of the most interesting problems in computing

You have used several compilers

What qualities are important in a compiler?

- 1. Correct code
- 2. Output runs fast
- 3. Compiler runs fast
- 4. Compile time proportional to program size
- 5. Support for separate compilation
- 6. Good diagnostics for syntax errors
- 7. Works well with the debugger
- 8. Good diagnostics for flow anomalies
- 9. Cross language calls
- 10. Consistent, predictable optimization

Each of these shapes your expectations about this course

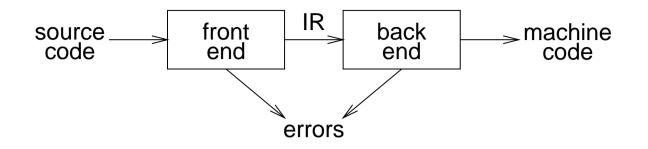


Implications:

- recognize legal (and illegal) programs
- generate correct code
- manage storage of all variables and code
- agreement on format for object (or assembly) code

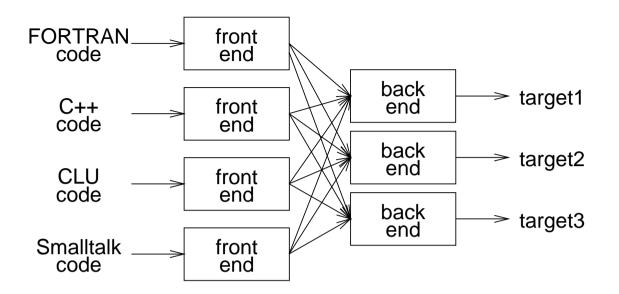
Big step up from assembler — higher level notations

### Traditional two pass compiler



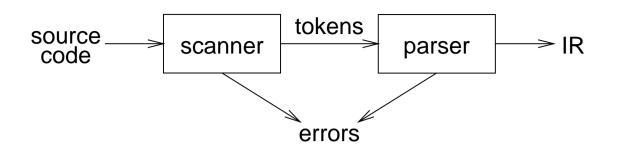
Implications:

- intermediate representation (IR)
- front end maps legal code into IR
- back end maps IR onto target machine
- simplify retargeting
- allows multiple front ends
- multiple passes  $\Rightarrow$  better code



Can we build  $n \times m$  compilers with n + m components?

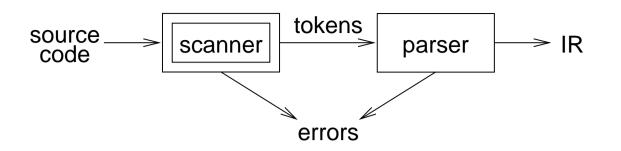
- must encode *all* the knowledge in each front end
- must represent *all* the features in one IR
- must handle all the features in each back end



Responsibilities:

- recognize legal procedure
- report errors
- produce IR
- preliminary storage map
- shape the code for the back end

#### Much of front end construction can be automated



Scanner:

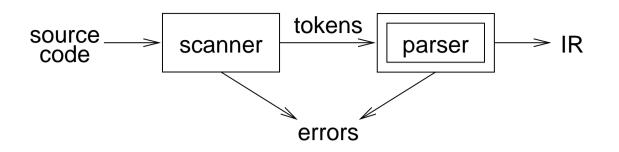
• maps characters into tokens – the basic unit of syntax

x = x + y;

becomes

 $<\!i\!d,\,x\!>$  =  $<\!i\!d,\,x\!>$  +  $<\!i\!d,\,y\!>$  ;

- character string value for a token is a lexeme
- typical tokens: *number*, *id*, +, -, \*, /, do, end
- eliminates white space (tabs, blanks, comments)
- a key issue is speed
  - $\Rightarrow$  use specialized recognizer (as opposed to lex)



Parser:

- recognize context-free syntax
- guide context-sensitive analysis
- construct IR(s)
- produce meaningful error messages
- attempt error correction

Parser generators mechanize much of the work

Context-free syntax is specified with a grammar

The noises sheep make under normal circumstances

This format is called *Backus-Naur form* (BNF)

Formally, a grammar G = (S, N, T, P) where

- *S* is the *start symbol*
- *N* is a set of *non-terminal symbols*
- T is a set of terminal symbols
- *P* is a set of productions or rewrite rules  $(P \cdot N \rightarrow N + T)$

 $(P:N\to N\cup T)$ 

Context free syntax can be put to better use

 $\begin{array}{c|ccccc} 1 & <goal> & ::= & <expr>\\ 2 & <expr> & ::= & <expr> <op> <term>\\ 3 & & & <term>\\ 4 & <term> & ::= & number\\ 5 & & & & & id\\ 6 & <op> & ::= & +\\ 7 & & & & & -\end{array}$ 

Simple expressions with addition and subtraction over tokens id and number

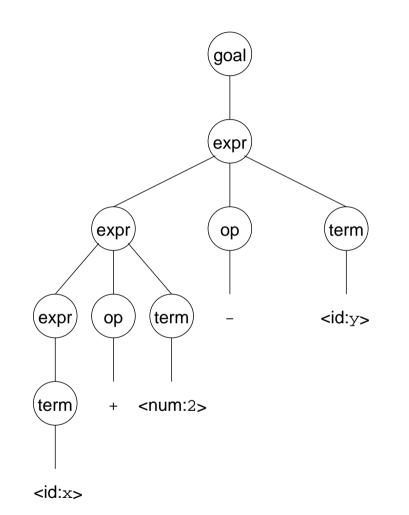
Given a grammar, valid sentences can be derived by repeated substitution.

Prod'n.	Result
	<goal></goal>
1	<expr></expr>
2	<expr> <op> <term></term></op></expr>
5	<expr> <op> y</op></expr>
7	<expr> - y</expr>
2	<expr> <op> <term> - y</term></op></expr>
4	<expr> <op> 2 - y</op></expr>
6	<expr> + 2 - y</expr>
3	<term> + 2 - y</term>
5	x + 2 - y

To recognize a valid sentence in some CFG, we reverse this process and build up a *parse* 

**Compiler Construction** 

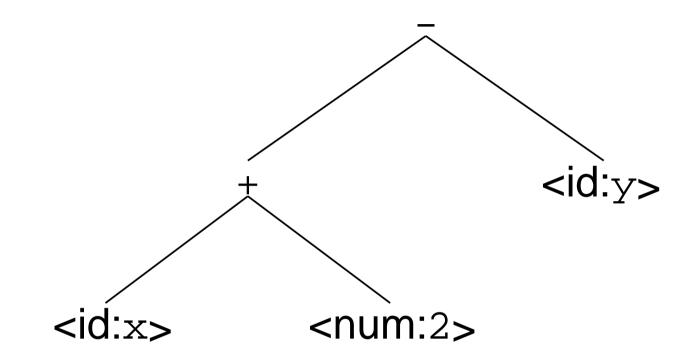
A parse can be represented by a *parse*, or *syntax*, tree



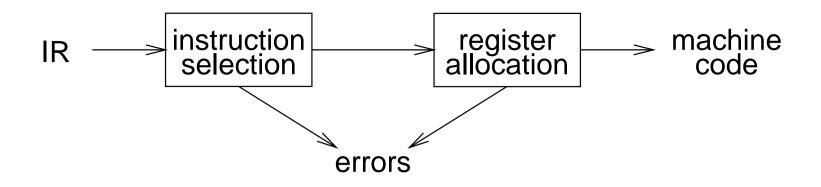
#### A parse tree contains a lot of unnecessary information

**Compiler Construction** 

So, compilers often use a more concise abstract syntax tree



Abstract syntax trees (ASTs) are often used as an IR between front end and back end

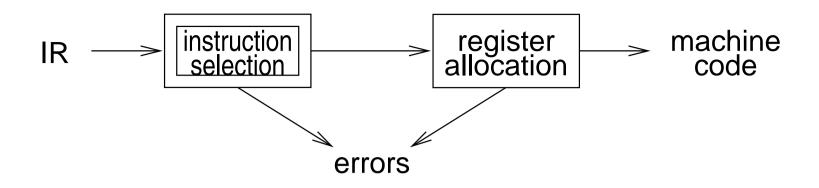


Responsibilities

- translate IR into target machine code
- choose instructions for each IR operation
- decide what to keep in registers at each point
- ensure conformance with system interfaces

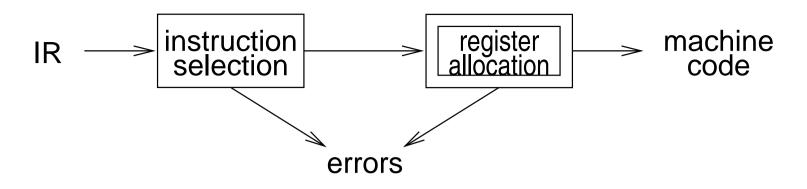
Automation has been less successful here

Compiler Construction



Instruction selection:

- produce compact, fast code
- use available addressing modes
- pattern matching problem
  - ad hoc techniques
  - tree pattern matching
  - string pattern matching
  - dynamic programming

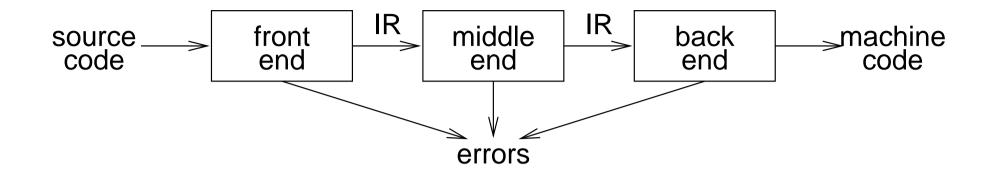


**Register Allocation:** 

- have value in a register when used
- limited resources
- changes instruction choices
- can move loads and stores
- optimal allocation is difficult

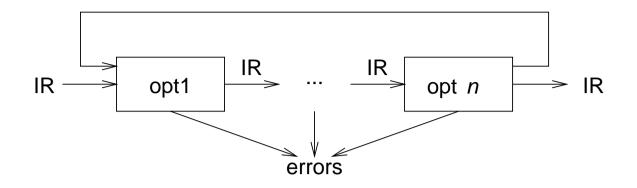
Modern allocators often rely on graph coloring

Compiler Construction



Code Improvement

- analyzes and changes IR
- goal is to reduce runtime
- must preserve values

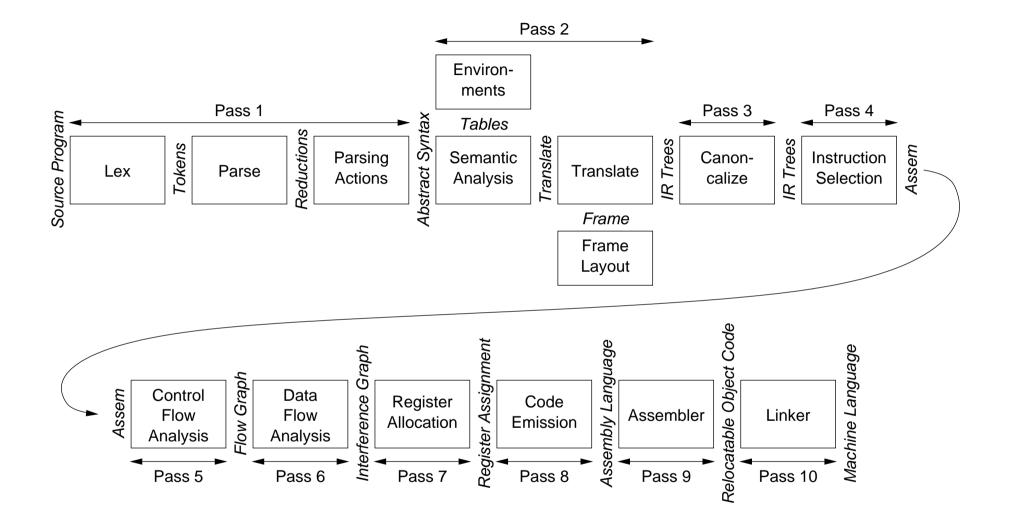


Modern optimizers are usually built as a set of passes

Typical passes

- constant propagation and folding
- code motion
- reduction of operator strength
- common subexpression elimination
- redundant store elimination
- dead code elimination

#### The MiniJava compiler



# The MiniJava compiler phases

Lex	Break source file into individual words, or <i>tokens</i>			
Parse	Analyse the phrase structure of program			
Parsing	Build a piece of abstract syntax tree for each phrase			
Actions				
Semantic	Determine what each phrase means, relate uses of variables to their defini-			
Analysis	tions, check types of expressions, request translation of each phrase			
Frame Layout	Place variables, function parameters, etc., into activation records (stack			
	frames) in a machine-dependent way			
Translate	Produce intermediate representation trees (IR trees), a notation that is not			
	tied to any particular source language or target machine			
Canonicalize	Hoist side effects out of expressions, and clean up conditional branches, for			
	convenience of later phases			
Instruction	Group IR-tree nodes into clumps that correspond to actions of target-			
Selection	machine instructions			
Control Flow	Analyse sequence of instructions into control flow graph showing all possi-			
Analysis	ble flows of control program might follow when it runs			
Data Flow	Gather information about flow of data through variables of program; e.g.,			
Analysis	liveness analysis calculates places where each variable holds a still-neede			
	( <i>live</i> ) value			
Register	Choose registers for variables and temporary values; variables not simulta-			
Allocation	neously live can share same register			
Code	Replace temporary names in each machine instruction with registers			
Emission				

# A straight-line programming language

Stm	$\rightarrow$	Stm; Stm	CompoundStm
Stm	$\rightarrow$	id := Exp	AssignStm
Stm	$\rightarrow$	<pre>print ( ExpList )</pre>	PrintStm
Exp	$\rightarrow$	id	IdExp
Exp	$\rightarrow$	num	NumExp
Exp	$\rightarrow$	Exp Binop Exp	OpExp
Exp	$\rightarrow$	(Stm, Exp)	EseqExp
ExpList	$\rightarrow$	Exp, ExpList	PairExpList
ExpList	$\rightarrow$	Exp	LastExpList
Binop	$\rightarrow$	+	Plus
Binop	$\rightarrow$	—	Minus
Binop	$\rightarrow$	X	Times
Binop	$\rightarrow$	/	Div
•	-		

An example straight-line program:

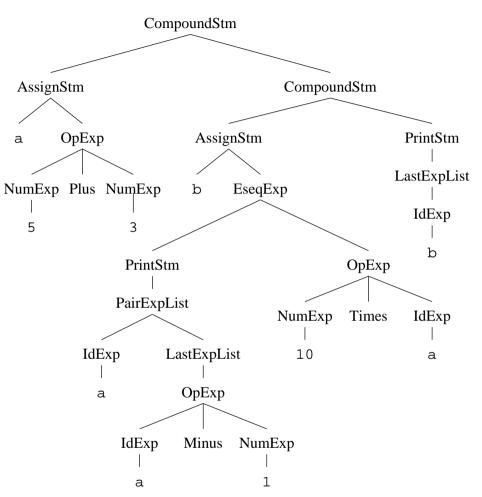
$$a := 5+3$$
;  $b := (print(a, a-1), 10 \times a)$ ;  $print(b)$ 

prints:

8 7

80

#### a := 5+3; $b := (print(a, a-1), 10 \times a)$ ; print(b)



This convenient internal representation is convenient for a compiler to use.

```
abstract class Stm {}
class CompoundStm extends Stm {
   Stm stm1, stm2;
   CompoundStm(Stm s1, Stm s2)
   { stm1=s1; stm2=s2; }
}
class AssignStm extends Stm {
   String id; Exp exp;
   AssignStm(String i, Exp e)
   { id=i; exp=e; }
}
class PrintStm extends Stm {
   ExpList exps;
   PrintStm(ExpList e)
   { exps=e; }
}
abstract class Exp {}
class IdExp extends Exp {
   String id;
   IdExp(String i) {id=i;}
}
```

```
class NumExp extends Exp {
   int num;
   NumExp(int n) {num=n;}
}
class OpExp extends Exp {
   Exp left, right; int oper;
   final static int
     Plus=1,Minus=2,Times=3,Div=4;
   OpExp(Exp 1, int o, Exp r)
   { left=1; oper=0; right=r; }
}
class EseqExp extends Exp {
   Stm stm; Exp exp;
   EseqExp(Stm s, Exp e)
   { stm=s; exp=e; }
}
abstract class ExpList {}
class PairExpList extends ExpList {
   Exp head; ExpList tail;
   public PairExpList(Exp h, ExpList t)
   { head=h; tail=t; }
}
class LastExpList extends ExpList {
   Exp head;
   public LastExpList(Exp h) {head=h;}
}
```